

Microelectronic Systems Education

June 2-4, 2007

Final Schedule

Saturday, June 2, San Diego Convention Center

Tutorials coordinated by Don Bouldin - dbouldin@tennessee.edu

08:30 - 12:00 (Room 23A): Cypress -- Programmable System-on-Chip

12:00 - 13:30: Lunch on your own

13:30 - 17:00 (Room 23A): Synopsys -- TCAD Sentaurus: Quick Start Workshop

13:30 - 17:00 (Room 23C): Synplicity -- Optimizing DSP Algorithms
for Hardware Implementation

Sunday, June 3, Marriott Hotel - San Diego Ballrooms A and B

San Diego Ballroom B for lunch and dinner, Ballroom A for all other events

8:00 - 8:30am Registration and Continental Breakfast

Setup time: Poster Session I and Speakers for Session 1
Poster presenters - see John Nestor for materials

8:30 - 10:40am Session 1: Embedded Systems

Introduction: Mark Johnson, Purdue University (General Chair)
mcjohnso@purdue.edu

8:40 Invited Speaker:

Dr. Pieter Mosterman, Senior Research Scientist,
The Mathworks, Inc.

9:20 Invited Speaker:

Dr. Stewart Tansley, External Research and Programs
Microsoft Research

Paper Presentations:

10:00 D. Helvick, R. Mercado, Z. Zhang, D. T. Rover
Iowa State University

10:20 P. Schaumont - Virginia Tech

10:40am - 12:00pm Session 2: Poster Session I

Poster Chair: John Nestor, Lafayette College
nestorj@lafayette.edu

12:00 - 1:30pm Lunch

12:00-12:20

Setup time for 2nd Poster session and EDA panel
Poster presenters - see John Nestor for materials
EDA Panel members - see James Duckworth

12:40 Luncheon Speakers

Dr. Andrzej Rucinski, University of New Hampshire
Dr. Don Bouldin, University of Tennessee

1:30 - 3:30pm Session 3 : EDA Panel Discussion

Moderator: James Duckworth, Worcester Polytechnic Institute
rjduck@ece.wpi.edu

3:40 - 5:00pm Session 4: Poster Session II

Poster Chair: John Nestor, Lafayette College

5:00 - 6:00pm Mixer

6:00 - 7:30pm Dinner

7:00 Dinner Speaker: Shay Benchorin, Synopsys

Monday, June 4, Marriott Hotel

San Diego Ballroom B for lunch and dinner, Ballroom A for all other events

8:00 - 8:30am Continental Breakfast

Setup time for Session 5. Session 5 speakers see Dave Parent
Session 6 speakers see Mihaela Radu

8:30 - 10:30am Session 5 : SoC

Moderator: Dave Parent, San Jose State University, dparent@email.sjsu.edu

8:30 Invited Speaker:

Dr. Stephen Brown, University of Toronto and Altera Toronto Technology Center

9:10 Invited Speaker:

Dr. Ivo Bolsen, Chief Technology Officer, Xilinx, Inc.

Regular paper presentations

9:50 N. McKeown, J. W. Lockwood, G. Watson, G. Gibb, P. Hartke,
J. Naous, R. Raghuraman, J. Luo - Stanford University

10:10 J. O. Hamblen - Georgia Tech

10:30 - 11:00am Break and setup for session 6

11:00am - 12:20pm Session 6: Innovative Teaching Methods and Curricula

Moderator: Mihaela Radu, Rose-Hulman Institute of Technology,
radu@rose-hulman.edu

11:00 M. Potkonjak, J. L. Wong - UCLA

11:20 J. A. Nestor - Lafayette College

11:40 C. M. Twigg, P. E. Hasler - Georgia Tech

12:00 J. E. Stine, I. Castellanos, W. R. Davis, P. D. Franzon,
M. Bucher, S. Basavarajaiah, J. Oh, R. Jenkal
Oklahoma State University

12:20pm Closing Remarks. Lunch on your own.

1:00 - 6:00pm Demos at the DAC Univ. Booth